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**1** [Modeling and analysis of differential signaling for minimizing inductive cross-talk](#)

Yehia Massoud, Jamil Kawa, Don MacMillen, Jacob White

June 2001 **Proceedings of the 38th conference on Design automation**

Full text available:  [pdf\(260.44 KB\)](#)

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Many physical synthesis tools interdigitate signal and power lines to reduce cross-talk, and thus, improve signal integrity and timing predictability. Such approaches are extremely effective at reducing cross-talk at circuit speeds where inductive effects are inconsequential. In this paper, we use a detailed distributed RLC model to show that inductive cross-talk effects are substantial in long busses associated with 0.18 micron technology. Simulation experiments are then used to demon ...



**2** [Operational features of an MOS timing simulator](#)

P. Kozak, H. K. Gummel, B. R. Chawla

January 1975 **Proceedings of the 12th conference on Design automation**

Full text available:  [pdf\(524.38 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes operational features of a timing simulator having performance characteristics between those of circuit analysis programs and conventional logic simulators.



**3** [Operational features of a MOS timing simulator](#)

P. Kozak, H. K. Gummel, B. R. Chawla

June 1988 **Papers on Twenty-five years of electronic design automation**

Full text available:  [pdf\(657.10 KB\)](#)

Additional Information: [full citation](#), [references](#), [index terms](#)



**4** [Interaction and modeling techniques for desktop two-handed input](#)

Ken Hinckley, Mary Czerwinski, Mike Sinclair

November 1998 **Proceedings of the 11th annual ACM symposium on User interface software and technology**

Full text available:  [pdf\(212.66 KB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



**Keywords:** TouchMouse, input devices, map navigation, tablests, three-state model, touchpads, two-handed input

**5** [Simultaneous shield insertion and net ordering for capacitive and inductive coupling minimization](#)



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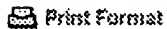
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